

REASON FOR ALLOWANCE

1. Claims 9-20 are allowable over cited prior art.

The following is an examiner's statement of reasons for allowance: The prior art of record (Richley, Kaczynski, Shohara and Wilhelmsson) fails to teach amplification device includes a first MOS-FET transistor having a drain connected to a source of a second MOS-FET transistor, a drain of the second MOS-FET transistor being connected to the output terminal, a source of the first MOS-FET transistor being connected to the input terminal, a gate of the first MOS-FET transistor being connected to a first bias terminal via a bias resistor and to the ground terminal via a first bias capacitor, the gate of the second MOS-FET transistor being connected to a second bias terminal and to the ground terminal via a second bias capacitor, as described in independent claims 9, 12, 13, 14, 19, 20. These limitations, in combination with the remaining limitations in claims 9, 12, 13, 14, 19, 20, are not taught nor suggested by the prior art of record.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to AJIBOLA AKINYEMI whose telephone number is (571)270-1846. The examiner can normally be reached on monday- friday (8.30-5pm) Est.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, DUC NGUYEN can be reached on (571) 272-7503. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/A. A./
Examiner, Art Unit 2618

/DUC NGUYEN/
Supervisory Patent Examiner, Art Unit 2618